



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Blackshear et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: VERTICALLY STACKED MEMORY CHIPS IN FBGA PACKAGES

Assistant Commissioner of Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0458.

EXPRESS MAIL NO.: EF255372713US

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING
DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS
MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER OF
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APPLICANT AND/OR ATTORNEY REQUESTS THE DATE OF
DEPOSIT AS THE FILE DATE.

DATE OF DEPOSIT: 1/24/02BY SIGNATURE & DATE: Frederick W. Gibb, III 1/24/02

Respectfully submitted,

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